

10/667,770

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6193	compar\$3 and (input signal\$1 with reference voltage\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
L2	27	programmable hysteresis circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
L3	6	I1 and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:40
L4	7	hysteresis delay circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L5	58833	digital-to-analog converter\$1 or DAC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L6	1	I3 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L7	2	I3 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:15
L8	3935	714/724 or 714/727 or "714"/ 327/77 or 327/205 or 327/72 or 327/68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:39

L9	3	I3 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:41
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L1	6193	compar\$3 and (input signal\$1 with reference voltage\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
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L5	58833	digital-to-analog converter\$1 or DAC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L6	1	I3 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L7	2	I3 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:15
L8	3935	714/724 or 714/727 or "714"/ 327/77 or 327/205 or 327/72 or 327/68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:39

L9	3	I3 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:43
L10	80314	voltage divider\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:43
L11	31740	current mirror\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L12	3342	scan chain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L13	1078	boundary scan test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L14	806	board under test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L15	33103	determin\$3 and noise level	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45
L16	143	hysteresis delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45

L17	1077	hysteresis voltage\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45
L18	1821	hysteresis circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45
L19	29314	default value\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L20	2840	I10 and I11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L21	4	I20 and I12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L22	1	I21 and I13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L23	1	I22 and I14 and I15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L24	1	I23 and (I16 or I17 or I18 or I19)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:47

L25	6	I3 and (I16 or I17 or I18 or I19)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:47
L26	3	I25 and (I10 or I11 or I12 or I13 or I14 or I15)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:48